

FIG. 1
(Background Art)

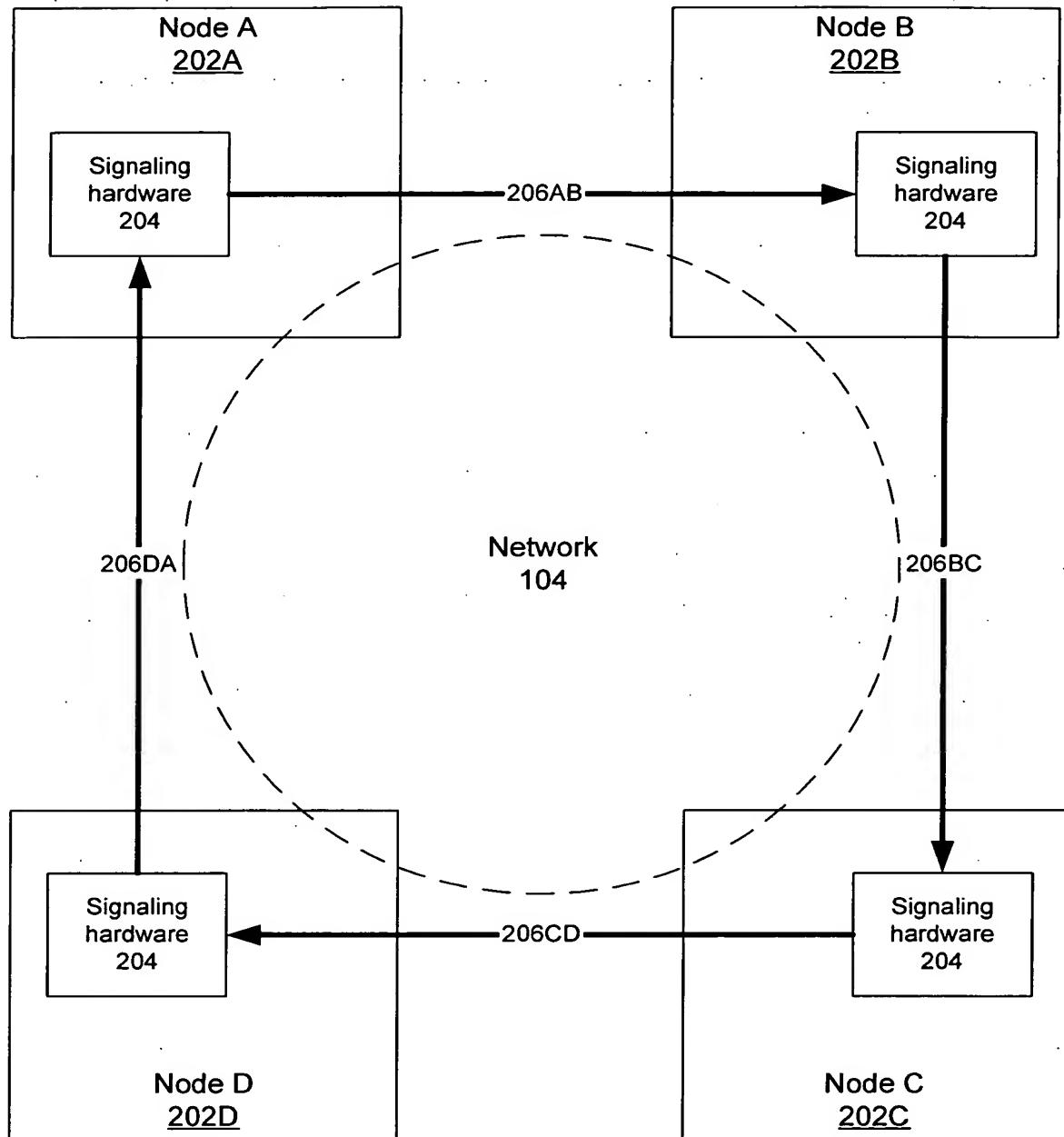


FIG. 2

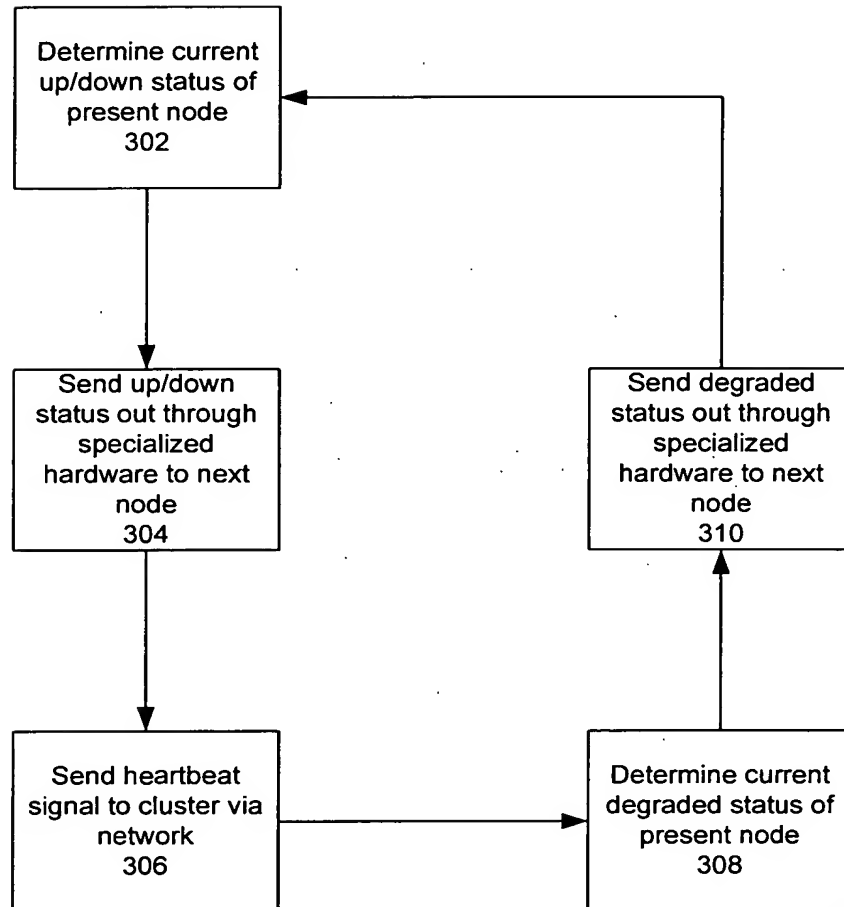
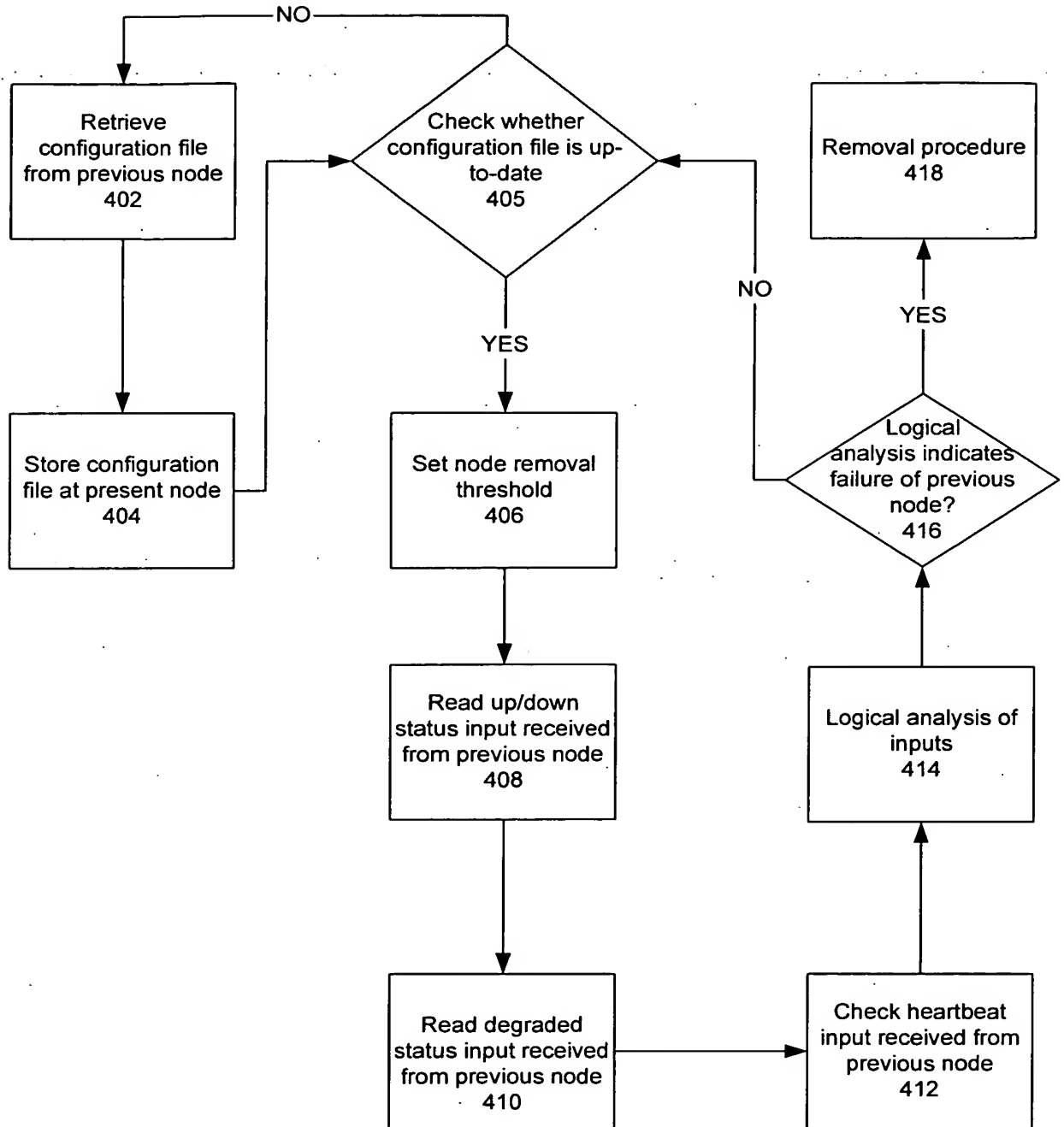


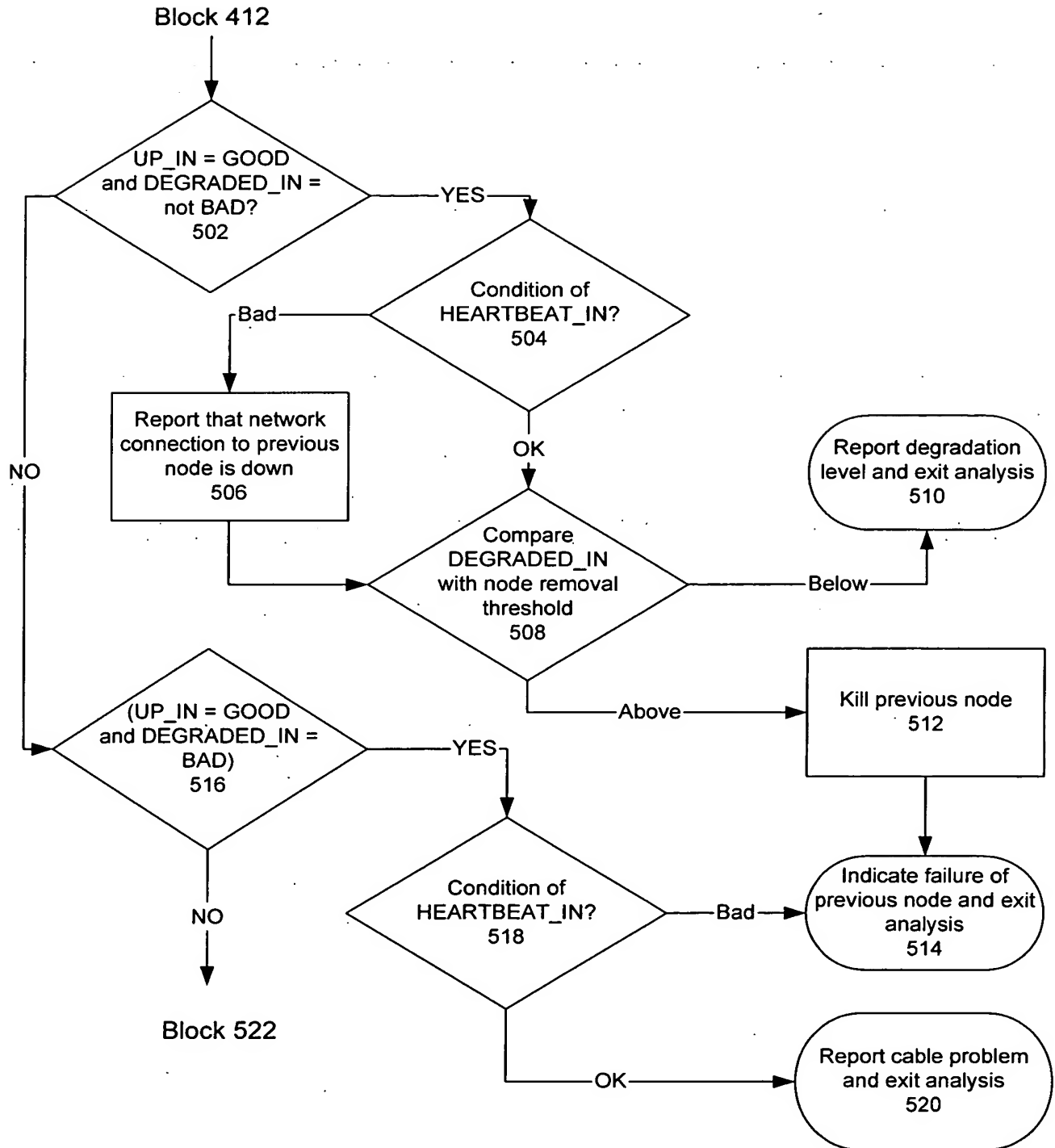
FIG. 3

Node Status Generation
Process
300



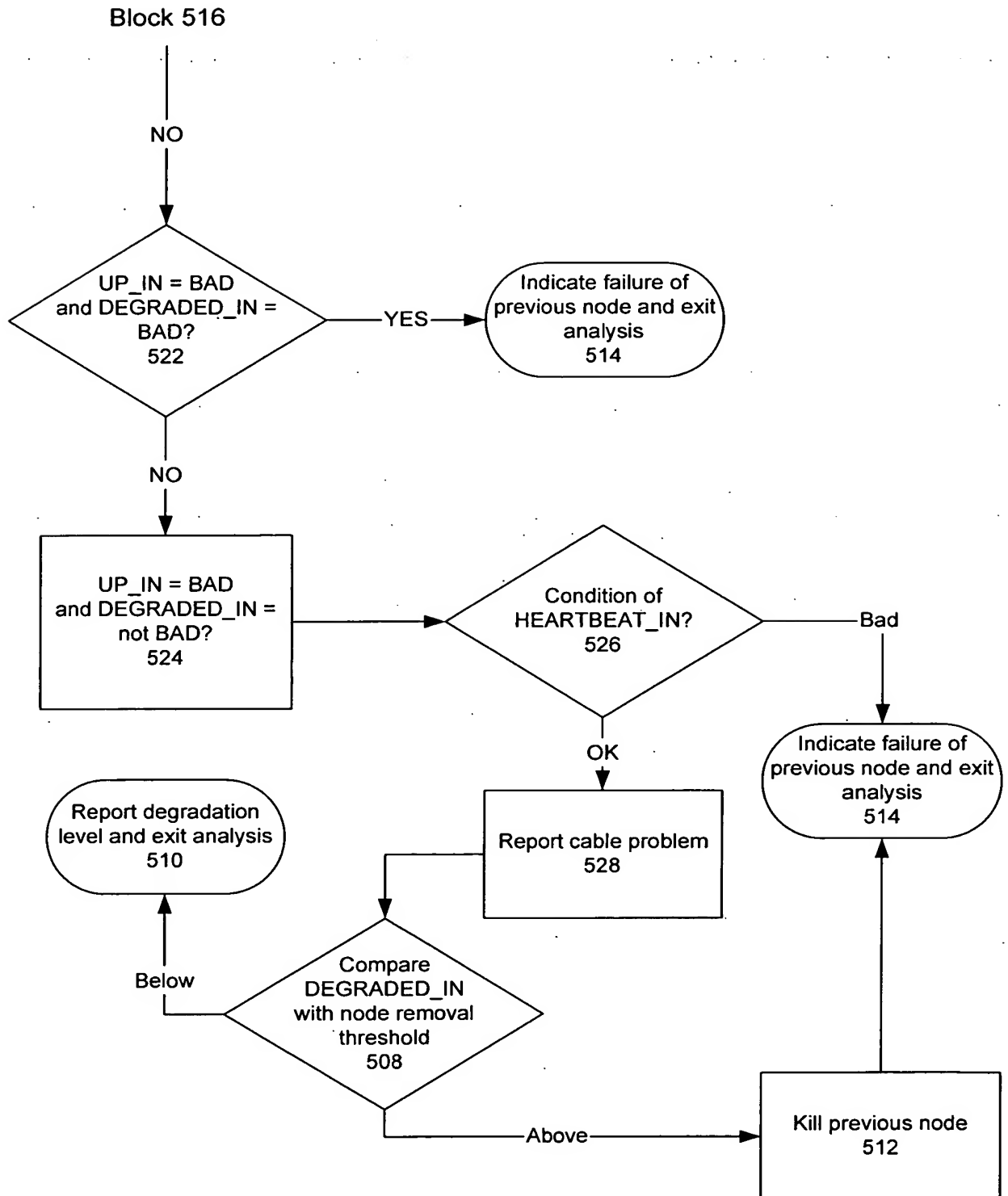
Cluster-Wide Management Process 400

FIG. 4



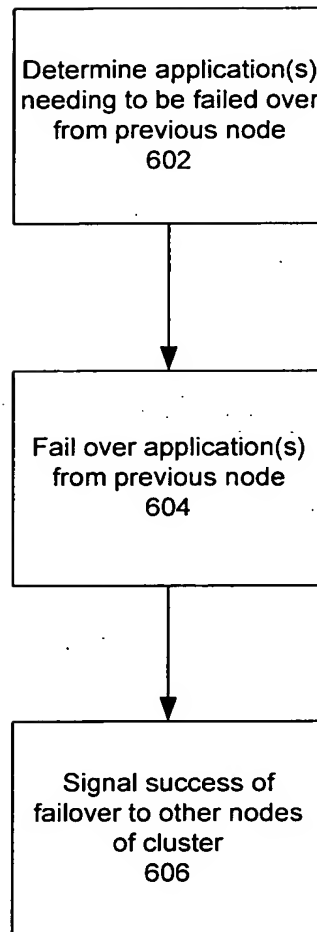
Logical Analysis

FIG. 5A



Logical Analysis

FIG. 5B



Removal Procedure

FIG. 6